

0057-2511-2811



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :  
SHUUCHI UENO ET AL : EXAMINER: LOKE, S.  
SERIAL NO: 09/395,184 :  
FILED: SEPTEMBER 14, 1999 : GROUP ART UNIT: 2811  
FOR: SEMICONDUCTOR DEVICE  
INCLUDING A WELL DIVIDED  
INTO A PLURALITY OF PARTS  
BY A TRENCH

#1810  
H. 1002

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

RECEIVED  
JUN 21 2002  
TECHNOLOGY CENTER 2800

SIR:

In response to the Office Action mailed February 19, 2002, please amend this application as follows:

IN THE CLAIMS

Please cancel Claims 2, 13-14, 17 and 21 without prejudice.

The following is a clean copy of Claims 1, 15-16, 20 and 22-23 with a marked-up copy attached.

1. (Twice Amended) A semiconductor device comprising:  
a semiconductor substrate;

an element isolation film formed such as to have a predetermined depth from a main surface of said semiconductor substrate, said element isolation film dividing the area from said main surface to said depth into a plurality of first regions;

C1

figs. 24, 10,  
14, 15,  
21, 22,  
23